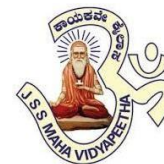




JSS Mahavidyapeetha
JSS Academy of Technical Education, Bengaluru Department of Electronics & Communication Engineering



FACULTY PROFILE

1. Personal Details

NAME	Dr Kavitha M	
DEPARTMENT	Electronics & Communication Engineering	
DESIGNATION	Assistant Professor	
PHONE	9449238353	
EMAIL ID	mkavitha@jssateb.ac.in	
TEACHING EXPERIENCE	23 Years	
INDUSTRY EXPERIENCE	Nil	
RESEARCH EXPERIENCE	10 Years	

2. Qualification

COURSES	SPECIALIZATION	INSTITUTION	UNIVERSITY
PhD	VLSI Design	BMSCE, Bangalore	VTU
M.Tech	VLSI Design & Embedded Systems	BMSCE, Bangalore	VTU
BE	Electronics & Communication Engineering	MVIT, Bangalore	Bangalore
PUC	PCMB	JNV, Mandya	CBSE
SSLC	X Standard	JNV, Mandya	CBSE

3. Membership of Professional Bodies:

1. Life Member ship in Indian Society for Technical Education(MISTE)
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4. Awards

Award Title	Date of receiving the award	Award issuing authority/Body/ Organization

5. Publications(Journals Conferences)

Sl. No.	Title of the paper	Name(s)of Author(s)	Name of the Journal	Volume No. Issue No. Year	WOS / Scopus/ Both	Impact Factor	Publisher
1	A 113.7 dB SINAD, 18.59 Bit $\Sigma\Delta$ Modulator for High-Resolution ECG Recordings	M. Kavitha, S. Akhila, and A. Kannan	ETASR	vol. 15, no. 5, pp. 28194–28200, Oct. 2025.	Scopus	1.5	Dionisios Pylarinos

2	Enhancing ECA (Event-Condition-Action) Rules: Fine-Tuning BERT for Security and Privacy Violation Detection	Hemavathi, Kalpana R, Kavitha M, Swetha Rani L	Journal of Information Systems Engineering and Management	2025,10(34s)e-ISSN:2468-4376,			
3	Optimized technique for the early identification of Parkinson's disease using machine learning-based handwriting and voice analysis	Nagarathna C R, Jobby George, Komala H K, Kavitha M, K R Swetha	International Journal of Innovative Research and Scientific Studies	,vol. 8 No. 3, pages: 2906-2916, ISSN 2617-6548, May 2025	Scopus		
4	Design and Implementation Of a Second Order Continuous-Time $\Sigma\Delta$ Modulator for ECG Signal Acquisition	M Kavitha, S Akhila, A Kannan	Engineering, Technology & Applied Science Research	Vol.13(1), 10128-10133	WoS & Scopus	1.5	Dionisios Pylarinos
5	A comprehensive review on Machine Learning algorithms for prediction and analysis of Chronic Kidney Disease	Kavitha M, Saroja S Bhusare, Ananya R, Jayashree P, L Ashwini, Niharika K	High Technology Letters	ISSNNO:1006-6748			
6	Analysis and Design of different OTA topologies for Bio-potential signals	Kavitha M, Dr S Akhila	International Journal of future generation and communication Networking	Vol.14, No.1, 2021	WoS	0.41	SERSC
7	Design and Performance analysis of a Low Power First order sigma Delta modulator	Kavitha M, Dr. H B Bhuvaneshwari	International Journal of Applied Engineering Research	Online ISSN : 0973-4562 Volume No. : 10 Issue No. : 92 Month & Year: 2015 Page Nos. : 284-290	Scopus	0.14	

8	An 8-bit area optimized low power continuous time band pass sigma-delta ADC in 45nm CMOS	Kavitha M, Rahul	International Journal of Information Technology and Computer Engineering	Online ISSN : 2319-6890 Volume No. : 4 Issue No. : 3 Month&Year: May 2015 Page Nos. : 155-159			
9	FPGA Implementation of Four-Channel Bit Error Rate Tester for Spacecraft Data Acquisition System	Kavitha M, Sinchana	International Journal of Information Technology and Computer Engineering	e- ISSN : 2455-529 Month&Year: 2016 Page Nos. : 109-113			

6. Grants/Funding Received

Grant Amount	Project Name	Date of receiving the Grant	Grant issuing authority/ Body / Organization

7. Others: Workshops/Conference (Orgained/Attended)

Workshops/Conference attended

Sl. No.	Title of the paper	Name(s) of Author(s)	Name of the Conference	Volume No. Issue No. Year	WOS / Scopus	Impact Factor	Publisher
1	Conversations with Context: A Modular Transformer Framework for Understanding and Generating Natural Language	Niranjan C Kundur, M Kavitha	ICMLDE 2025 conducted at the School of Computer Science, UPES from 6-8 November 2025				
2	Real-Time Disease Prediction With Machine Learning Algorithms	Dr.Dankan V Gowda, M Kavitha, Sriyogi Kottala, R Nithya	IGI Global Publishers		Scopus		
3	Intelligent Fault Detection and Prediction in Smart Grids Using Supervised Learning Model	S M Usha; D Mahesh Kumar; M Kavitha; G S Pavithra; S	2024 International Conference on Recent Advances in Science and Engineering Technology		Scopus		

		Mallikarjunaswamy; N Sharmila					
4	A machine learning based power load prediction system for smart grid energy management	S Jyothi, S Mallikarjunaswamy, M Kavitha, N Kumar, N Sharmila	2023 International Conference on Recent Advances in Science and Engineering		Scopus		
5	Restoration of Images Corrupted by Multipath Fading Channel with Weighted Encoding	V Yatnalli, S S Bhusare, M Kavitha, A Naik, T Ashwini, D Chandana	2022 Fourth International Conference on Emerging Research in Electronics		WOS & Scopus		
6	Flexible Analog Front End Architecture for Biomedical Applications	M Kavitha, S Akhila, Anand Kannan	2021 IEEE 3 rd PhD Colloquium on Ethically Driven Innovation and Technology		WOS & Scopus		
7	Switched Capacitor Implementation of First Order Sigma-Delta Modulator	Kavitha M, Dr S Akhila	2018 Third International Conference on Electrical, Electronics, Communication, Computer Technologies and Optimization Techniques (ICEECCOT) 14-15, December 2018				
8	FPGA Implementation of Four-Channel Bit Error Rate Tester for Spacecraft Data Acquisition System	Kavitha M, Sinchana	4 th National Conference on "Recent Advances in Science, Engineering and Technology (NCRASET-16)" organized by Sri Venkateshwara College of Engineering, Bangalore.				
9	An 8-bit area optimized low power continuous time band pass sigma-delta ADC in 45nm CMOS	Kavitha M, Rahul	2 nd International Conference on Convergent Innovative Technologies ICCIT-2015 held at Cambridge Institute of Technology, Bangalore				

10	FPGA implementation 4-channel bit error rate tester for space trak data aquasition system	Kavitha M, Madhusudhan	NCRASET-16				
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Conference Attended (those sponsored by AICTE/ISTE/IETE/TEQIP or any others sponsoring body)

Sl. No.	Name of the workshop/Conference	Organiser	Date
1	Enhancing Cybersecurity Awareness through lightweight Cryptographic Techniques for IoT Applications,	NIT Rourkela	Jan-30 to Feb-3rd, 2026
2	Trends and Challenges in Semiconductor Technology, Artificial Intelligence and Machine Learning: Research Perspective,	JSSATEB in association with Electronics and ICT Academy, NIT Warangal	23 rd June 2025 to 2 nd July 2025
3	VLSID 2025	International Conference organized at The Leela Palace Bengaluru	4-8 January 2025.
4	Novel Nanomaterials, Devices and VLSI Circuits: Research Advancements and Applications”	Electronics and ICT Academy, NIT Warangal	4-13 March 2025
5	Research trends and applications of Nano scale devices and circuits	Department of Electronics and Communication Engineering, NIT, Warangal	20 th to 31 th July 2024
6	Inculcating Universal Human Values in Technical Education	All India Council for Technical Education (AICTE)	12 th June to 16 th June 2023
7	Analog VLSI Design using EDA Tools	Visvesvaraya Technological University, Centre for PG Studies, VIAT, Muddenahalli, Chikkaballapur (Dist.)	20 th to 25 th March 2023
8	Importance of data science in Machine learning and its applications	R V Institute of Technology and Management, Bengaluru	6 th to 10 th Feb 2023
9	Design of low power analog integrated circuits	Gauhati University Institute of Science and Technology	29/11/2021 to 03/12/2021, 5 days
10	The Science of Well-Being	Coursera, Yale University	18/10/2021 to 29/12/2021, 6 weeks
11	Machine Learning for all	Coursera, University of London	06-08-2020 (4 weeks)
12	Six days short term programming programme on Industry 4.0 sponsored by AICTE	Department of Electronics and Communication Engineering, ACSCE, Bangalore	9 th to 14 th November 2020
13	AI and ML applications in Image processing using modern tools	M S Ramaiah Institute of technology, Bangalore	13 th to 18 th July 2020
14	Three Day Faculty Development Program on “Analog and Digital System Design using Cadence Tool”	Department of Electronics and Communication Engineering, GSSSIETW, Mysore	23 rd to 25 th January 2019
15	5 Days Faculty Development Program on “ GNU Radio and Software Defined Radio”	Department of Electronics & Communication Engineering at JSSATE, Bangalore.	27 th June 2016 to 1 st July 2016
16	One week workshop on “Electronics System Design, Manufacturing & Testing (ESDMT)”	Department of Electronics & Instrumentation Engineering at BMSCE, Bangalore in association with Entuple Technologies.	27 th to 31 st July 2015
17	Didactic Seminar on “Cadence OrCAD PSPICE and ORCAD PCB”.	Department of Electronics & Instrumentation Engineering at BMSCE, Bangalore	31 st July 2015

18	“Signal, Image Processing and SDR using Lab View”.	Department of Electronics & Communication Engineering at JSSATE, Bangalore.	22 nd to 24 th June 2015
19	3 day Faculty Development Program on “Advanced VLSI Design using Cadence Tool Suite”	Department of Electronics & Communication Engineering at JSSATE, Bangalore..	16 th , 17 th & 18 th July 2014
20	5 day Hands-on workshop on “Analog & Digital Chip Design Using Cadence”.	Department of Electronics & Communication Engineering at Nitte Meenakshi Institute of Technology, Bangalore	22 nd to 26 th July 2014
21	Participated as a Delegate in the 26 th Indian Engineering Congress.	Organized by Indian Engineering Congress, Bangalore.	15 th to 18 th December 2011

workshop/Seminar/Conference Attended(those NOT sponsored by AICTE/ISTE/IETE/TEQIP or any other sponsoring body)

PROJECT/ Innovations/Patents

1.Design Of High Resolution Sigma-Delta Modulator For ECG Recordings
2.Machine Learning Based Audio Device for Teaching Instruction Messages

Any other information you will like to share about your professional experience